

LARG Electronics 2003-2004

Test		2003						2004			Total	Extra for Barrel TB	Extra for Endcap TB
		FEC tests	HAD tests	FCAL Calib	B180 (EMB)	Barrel TR	EC TR	B180 (EMEC)	Barrel TB	Endcap TB			
FEB	FEB (RadT)	17	0	0	14	1	0	12	16	0	17	0	0
	FEB (M0, with conv)	0	3	8	0	0	1	0	0	8	8	0	8+
FEC	FEC (RadT)	1	0	0	1	1	0	1	1	0	3	1	0
	FEC (M0)	0	1	1	0	0	1	0	0	1	1	0	0
	Baseplanes (RadT)	4	1 (HEC)	1 (FCAL)	mounted	1 (EMB)	1(FCAL or EC)	mounted	2 (EMB)	2 (FCAL,EC)	8	2	1
Other	Controller	2	0	0	1	1	0	1	2	0	4	2	0
FEC Modules	Calib Board (RadT)	1	0	0	1	1	0	1	2	0	3	1	0
	Calib Board (M0)	0	1	1*	0	0	1	0	0	2+1*	2+1*	0	0
	TBB (RadT)	2	0	0	0	1	0	0	2	0	3	1	0
	TDB (RadT)	1	2	0	0	0	0	0	0	0	3	0	0
	Temp Mon (RadT)	0	0	0	0	1	0	0	1	0	1	1	0
	Volt Mon (RadT)	1	0	0	0	1	0	0	1	0	2	1	0
	LV DC supply (RadT)	1	0	0	0	1	0	1	1	0	3	1	0
Readout DB	ROD Demo DB	8	6	0	7	0	0	6	0	0	8	0	0
	ATLAS ROD DB	0	0	0	0	1	1	0	4	2	6	4	2
Readout Modules	Minirods	0	0	8	0	0	0	0	0	0	8	0	0
	ROD Demo MB	1	2	0	0	0	0	0	0	0	2	0	0
	9U VME ROD Demo	2	0	0	2	0	0	2	0	0	2	0	0
	ATLAS ROD	0	0	0	0	1	1	0	2	1	3	2	1
	ATLAS TM	0	0	0	0	1	1	0	2	1	3	2	1
	ROD Injector	0	0	0	0	1	1	0	1	1	2	1	1
Control	TB TTC dist	0	1	1	0	0	1	0	0	1	1	0	0
	LTP	0	0	0	0	1	1	1	1	1	2	1	1
	TBM	0	0	0	0	0	1	0	1	1	2	1	1
	6U SPACMaster	0	1	1	0	0	1	0	0	1	1	0	1
	9U SPACMaster	1	0	0	1	1	0	1	1	0	3	1	0
TTC Crate	TTCvi	1	1	1	1	1	1	1	1	1	4	1	1
	TTCex	0	0	0	1	1	1	1	1	1	3	1	1
	TTCvx	1	0	0	0	0	0	0	0	0	1	0	0
	TTCoc x32	0	0	0	0	1	1	0	1	1	2	1	1
Readout Crate	6U VME standard	1	1	1	1	1	1	1	1	1	4	0	0
	9U VME standard	1	0	0	2	0	0	2	0	0	2	0	0
	9U VME (ROD)	0	0	0	0	1	1	0	1	1	2	2	1
L1 Trigger Sums	Receiver MB	1	1	0	0	1	0	0	1	0	2	1	0
	Receiver DB	2	4	0	0	1	0	0	4	0	10	4	0
	LARG TSum Digitizer	1	1	0	0	0	0	0	0	0	2	0	0
	Receiver Crate	0	0	0	0	1	0	0	1	0	1	1	0

*Modified for FCAL

+Elec.-Opt. +Elec.-Opt.

EC=HEC or EMEC standard baseplane

Converter

Converter